

Figure 1. TLB and TLB registers in the Base
TLB architecture

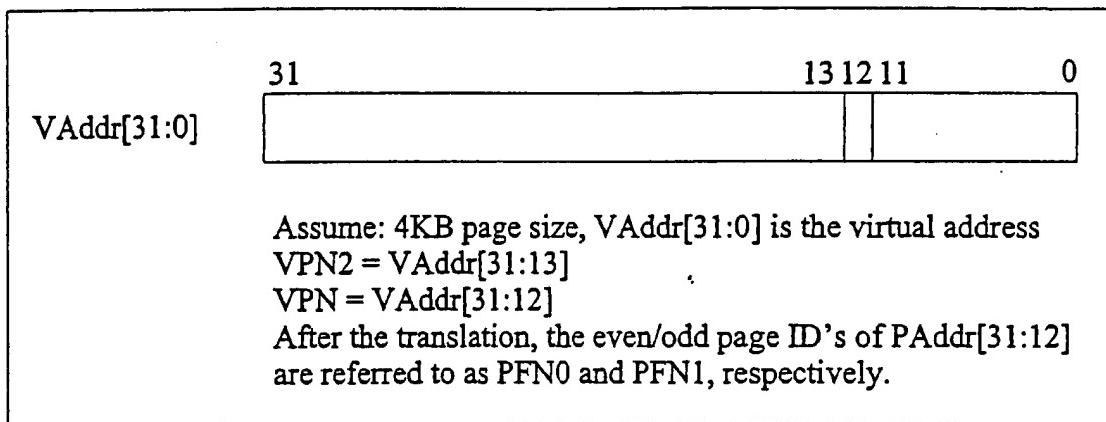


Figure 2. 32-bit Virtual Address to a Mini-TLB

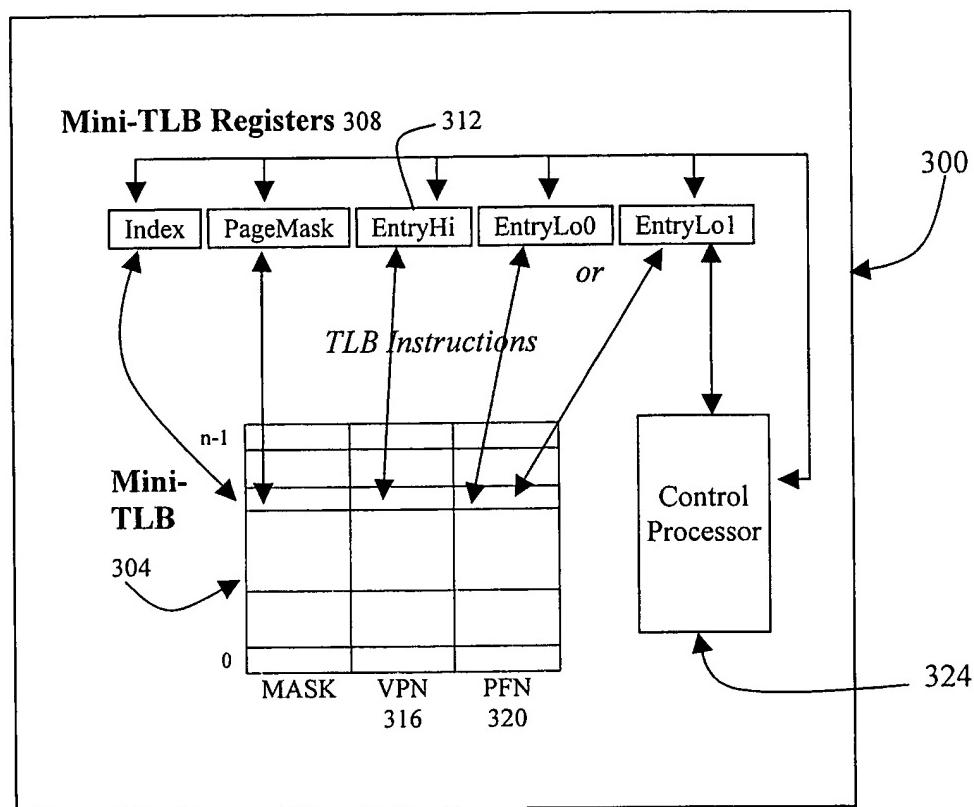


Figure 3. Mini-TLB System: Mini-TLB,
associated TLB registers, and Control Processor

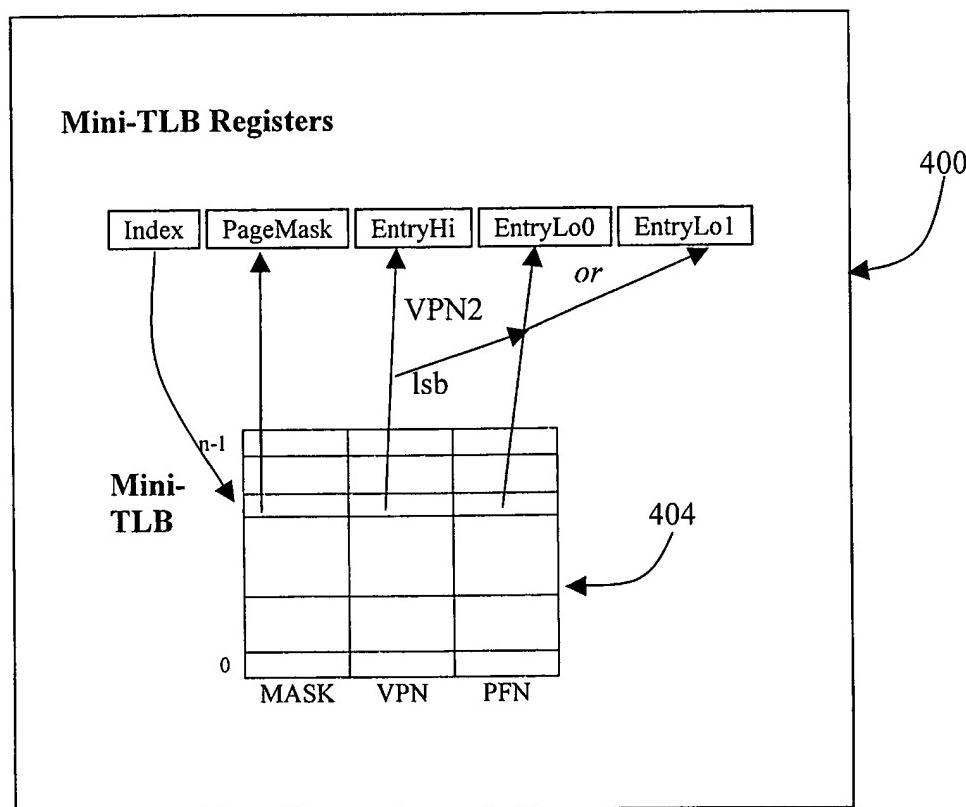


Figure 4. TLBR Operation

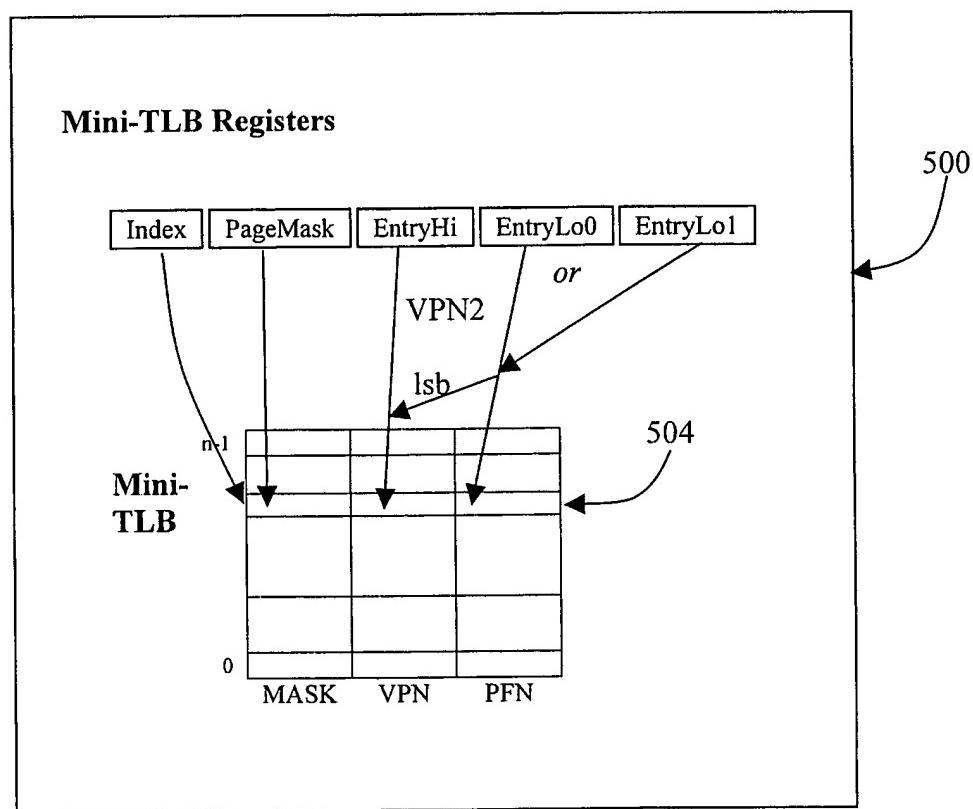


Figure 5. TLBWI or TLBWR Operation

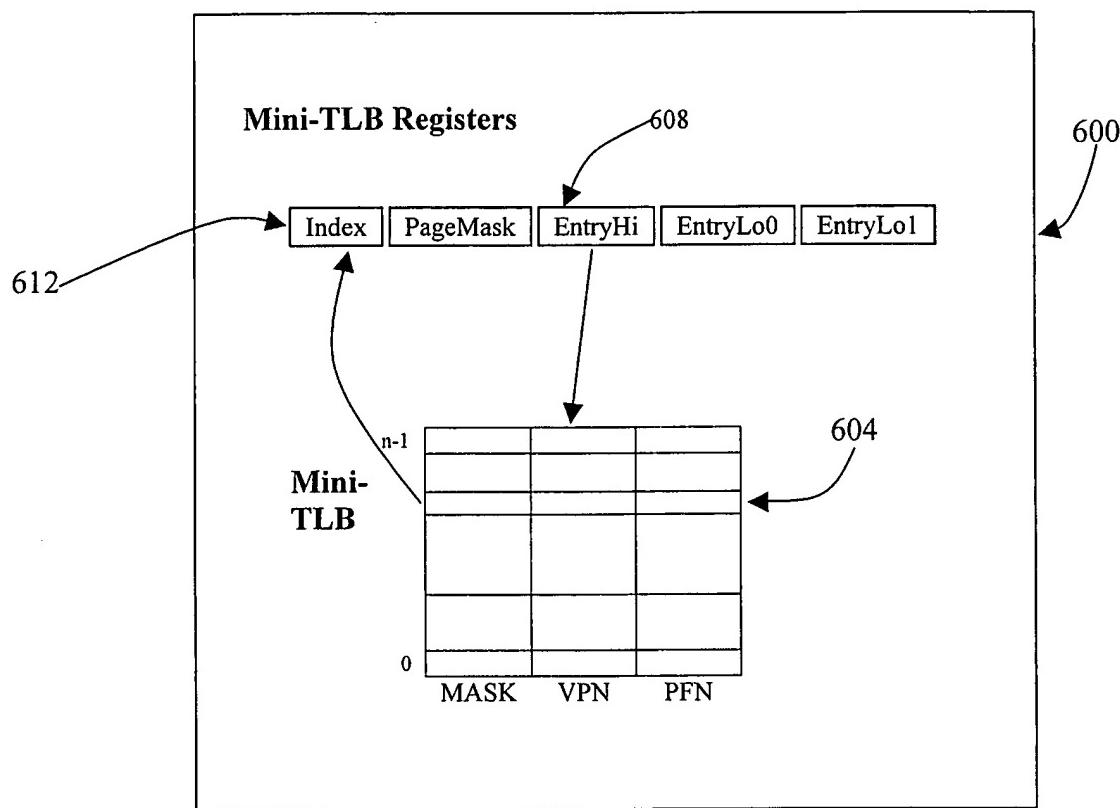


Figure 6. TLBP Operation